

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	<b>METHOD AND CIRCUIT FOR PRECISE TIMING OF SIGNALS IN AN EMBEDDED DRAM ARRAY</b>
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant:        Darren L. Anand</p> <p>Confirmation Number:</p> <p>Attorney Docket Number:        BUR920020144US1</p>	
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<b>Submitted By:</b>	<b>Elec. Sign.</b>
Robert A. Walsh Registered Number: 26,516	Robert A. Walsh /s/ Attorney

Documents being submitted:	Files
us-assignment	BUR920020144US1-usassn.xml us-assignment.xsl us-assignment.dtd
us-request	BUR920020144assign.tif BUR920020144US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920020144US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	BUR920020144SPEC-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920020144drawing1.tif BUR920020144drawing2.tif BUR920020144drawing3.tif BUR920020144drawing4.tif BUR920020144drawing5.tif BUR920020144drawing6.tif
us-declaration	BUR920020144US1dec1.tif
us-declaration	BUR920020144US1dec2.tif
us-declaration	BUR920020144US1dec3.tif
us-declaration	BUR920020144US1dec4.tif
<b>Comments</b>	

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